

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of:	Chamberlain et al.	Group No.:	
Serial No.:		Atty. Docket No.:	53047-57370
Filed:	21 May 2004		
For:	Intelligent Data Storage and Processing Using FPGA Devices	Examiner:	

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Commissioner of Patents and Trademarks
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PRELIMINARY AMENDMENT A

Applicant respectfully requests that the following preliminary amendment be entered and considered prior to calculation of the filing fee for the above-identified application.

In The Claims:

The claims of the application have been amended herein as indicated in the following marked-up copies of the claims:

1. (Original) A programmable logic device in communication with a mass storage medium, said device being configured to manipulate data passing to or from said mass storage medium in a continuous data stream.

2. (Original) The device of claim 1 wherein said data manipulation includes at least a search operation.

3. (Original) The device of claim 2 wherein said data stream includes encrypted data thereon, and wherein the device is configured to crypto-search said data.